

# Inventor Information for 10/084596

Inventor Name	City	State/Country
GULICK, DALE E.	AUSTIN	TEXAS

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## Inventor Name Search Result

Your Search was:

Last Name = GULICK

First Name = DALE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07564048</a>	Not Issued	166	08/08/1990	PROGRAMMABLE KEYPAD MONITOR	GULICK, DALE
<a href="#">07569935</a>	Not Issued	166	08/20/1990	POWER-EFFICIENT EXTERNAL MEMORY ACCESS CONTROL USING EXTERNAL MEMORY ENABLE TIME DURATIONS INDEPENDENT OF EXTERNAL MEMORY ACCESSING RATE	GULICK, DALE
<a href="#">07942359</a>	5266950	150	09/09/1992	PROGRAMMABLE KEYPAD MONITOR	GULICK, DALE
<a href="#">08140820</a>	5481690	150	10/04/1993	POWER-EFFICIENT EXTERNAL MEMORY ACCESS CONTROL USING EXTERNAL MEMORY ENABLE TIME DURATIONS INDEPENDENT OF EXTERNAL MEMORY ACCESSING RATE	GULICK, DALE
<a href="#">08264883</a>	5535281	150	06/24/1994	ANALOG INTEGRATED CIRCUIT	GULICK, DALE
<a href="#">08479780</a>	Not Issued	168	06/07/1995	COMPUTER SYSTEM AND METHOD FOR TRANSFERRING COMMANDS AND DATA TO A DEDICATED MULTIMEDIA ENGINE	GULICK, DALE
<a href="#">08479781</a>	Not Issued	161	06/07/1995	PROGRAMMABLE BUS ARBITER INCLUDING REAL TIME PRIORITY INDICATOR FIELDS FOR ARBITRATION PRIORITY SELECTION	GULICK, DALE
<a href="#">08481705</a>	Not Issued	166	06/07/1995	COMPUTER SYSTEM HAVING A DEDICATED MULTIMEDIA ENGINE INCLUDING MULTIMEDIA MEMORY	GULICK, DALE
<a href="#">08481706</a>	Not Issued	161	06/07/1995	COMPUTER SYSTEM HAVING AN IMPROVED BUS ARBITER ADAPTED FOR REAL TIME APPLICATIONS	GULICK, DALE
<a href="#">08878229</a>	5732224	150	06/18/1997	COMPUTER SYSTEM HAVING A DEDICATED MULTIMEDIA ENGINE INCLUDING MULTIMEDIA MEMORY	GULICK, DALE

<u>08948639</u>	<u>5870622</u>	150	10/10/1997	COMPUTER SYSTEM AND METHOD FOR TRANSFERRING COMMANDS AND DATA TO A DEDICATED MULTIMEDIA ENGINE	GULICK, DALE
<u>09220487</u>	<u>6167492</u>	150	12/23/1998	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES COUPLED TO A MULTIPROCESSOR SYSTEM	GULICK, DALE
<u>09382360</u>	<u>6389526</u>	150	08/24/1999	CIRCUIT AND METHOD FOR SELECTIVELY STALLING INTERRUPT REQUESTS INITIATED BY DEVICES COUPLED TO A MULTIPROCESSOR SYSTEM	GULICK, DALE
<u>09832374</u>	Not Issued	161	04/11/2001	Soft magnetic compound for injection molded return paths	GULICK, DALE
<u>10718166</u>	Not Issued	20	11/20/2003	Soft magnetic compound for injection molded return paths	GULICK, DALE
<u>09098570</u>	<u>6134698</u>	150	06/17/1998	REDUCED PIN COUNT ISOCHRONOUS DATA BUS	GULICK, DALE E.
<u>08929153</u>	<u>6601178</u>	150	09/11/1997	SYSTEM POWER MANAGEMENT PARTITIONED ACROSS A SERIAL BUS	GULICK, DALE E
<u>09094016</u>	<u>6502123</u>	150	06/09/1998	ISOCHRONOUS SYSTEM USING CERTIFIED DRIVERS TO ENSURE SYSTEM STABILITY	GULICK, DALE E
<u>09501888</u>	<u>6195749</u>	150	02/10/2000	Computer system including a memory access controller for using non-system memory storage resources during system boot time	GULICK, DALE E.
<u>09502068</u>	<u>6532019</u>	150	02/10/2000	INPUT/OUTPUT INTEGRATED CIRCUIT HUB INCORPORATING A RAMDAC	GULICK, DALE E.
<u>09544858</u>	<u>6952751</u>	150	04/07/2000	METHOD AND APPARATUS FOR EXTENDING LEGACY COMPUTER SYSTEMS	GULICK, DALE E.
<u>09564765</u>	Not Issued	121	05/04/2000	Memory controller providing multiple power modes for accessing memory devices by reordering memory transactions	GULICK, DALE E.
<u>09577523</u>	Not Issued	161	05/24/2000	Graphics subsystem including a RAMDAC IC with a dedicated vsync interrupt structure for connection to a graphics bus	GULICK, DALE E.
<u>09577524</u>	Not Issued	161	05/24/2000	Graphics subsystem including a pull-mode RAMDAC for connection to a graphics bus	GULICK, DALE E.
<u>09577525</u>	Not Issued	161	05/24/2000	Graphics subsystem including a dual pull-mode RAMDAC for connection	GULICK, DALE E.

				to a graphics bus	
<u>09577526</u>	Not Issued	161	05/24/2000	Graphics subsystem including a RAMDAC IC including integrated video overlay circuitry for connection to a graphics bus	GULICK, DALE E.
<u>09577527</u>	6798418	150	05/24/2000	GRAPHICS SUBSYSTEM INCLUDING A RAMDAC IC WITH DIGITAL VIDEO STORAGE INTERFACE FOR CONNECTION TO A GRAPHICS BUS	GULICK, DALE E.
<u>09636417</u>	Not Issued	161	08/10/2000	High speed data bus incorporating series resistor coupled bus devices	GULICK, DALE E.
<u>09652647</u>	6760852	150	08/31/2000	SYSTEM AND METHOD FOR MONITOING AND CONTROLLING A POWER-MANAGEABLE RESOURCE BASED UPON ACTIVITIES OF A PLURALITY OF DEVICES	GULICK, DALE E.
<u>09702147</u>	6385705	150	10/30/2000	CIRCUIT AND METHOD FOR MAINTAINING ORDER OF MEMORY ACCESS REQUESTS INITIATED BY DEVICES IN A MULTIPROCESSOR SYSTEM	GULICK, DALE E.
<u>09715946</u>	Not Issued	161	11/17/2000	System and method for enabling enumeration of physically partitioned devices	GULICK, DALE E.
<u>09747016</u>	Not Issued	167	12/20/2000	MONOLITHIC PC AUDIO CIRCUIT	GULICK, DALE E.
<u>09772423</u>	6473663	150	01/29/2001	NOISE ELIMINATION IN A USB CODEC	GULICK, DALE E.
<u>09798456</u>	6816935	150	03/02/2001	INTERRUPT AND STATUS REPORTING STRUCTURE AND METHOD FOR A TIMESLOT BUS	GULICK, DALE E.
<u>09798457</u>	Not Issued	161	03/02/2001	Chipset architecture with plug-and-play configuration registers	GULICK, DALE E.
<u>09798458</u>	Not Issued	161	03/02/2001	Time division bus host controller architecture	GULICK, DALE E.
<u>09798480</u>	Not Issued	161	03/02/2001	Data protocol structure for a time division bus	GULICK, DALE E.
<u>09852372</u>	Not Issued	120	05/10/2001	Secure execution box	GULICK, DALE E.
<u>09852942</u>	Not Issued	61	05/10/2001	Computer system architecture for enhanced security and manageability	GULICK, DALE E.
<u>09853225</u>	Not Issued	71	05/11/2001	Secure system mode duration management	GULICK, DALE E.
<u>09853234</u>	6862641	150	05/11/2001	INTERRUPTABLE AND RE-ENTERABLE SYSTEM MANAGEMENT MODE PROGRAMMING CODE	GULICK, DALE E.
<u>09853335</u>	Not Issued	121	05/11/2001	Asset sharing between host processor and security hardware	GULICK, DALE E.

<u>09853395</u>	Not Issued	41	05/11/2001	Enhanced security and manageability using secure storage in a personal computer system	GULICK, DALE E.
<u>09853437</u>	6832317	150	05/11/2001	PERSONAL COMPUTER SECURITY MECHANISM	GULICK, DALE E.
<u>09853443</u>	Not Issued	123	05/11/2001	Protection mechanism for biometric input data	GULICK, DALE E.
<u>09853446</u>	Not Issued	71	05/11/2001	Resource sequester mechanism	GULICK, DALE E.
<u>09853447</u>	6823451	150	05/11/2001	INTEGRATED CIRCUIT FOR SECURITY AND MANAGEABILITY	GULICK, DALE E.
<u>09854040</u>	Not Issued	95	05/11/2001	CRYPTOGRAPHIC RANDOMNESS REGISTER FOR COMPUTER SYSTEM SECURITY	GULICK, DALE E.
<u>09870889</u>	Not Issued	71	05/30/2001	External locking mechanism for personal computer memory locations	GULICK, DALE E.
<u>09870890</u>	Not Issued	61	05/30/2001	Secure booting of a personal computer system	GULICK, DALE E.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>09871084</u></a>	Not Issued	61	05/30/2001	Locking mechanism override and disable for personal computer ROM access protection	GULICK, DALE E.
<a href="#"><u>09871511</u></a>	Not Issued	41	05/30/2001	Monotonic counter mechanism for computer system security	GULICK, DALE E.
<a href="#"><u>09904373</u></a>	Not Issued	61	07/11/2001	Method and apparatus for providing capability information to a shared controller	GULICK, DALE E.
<a href="#"><u>09904374</u></a>	6671748	150	07/11/2001	METHOD AND APPARATUS FOR PASSING DEVICE CONFIGURATION INFORMATION TO A SHARED CONTROLLER	GULICK, DALE E.
<a href="#"><u>09933290</u></a>	6625743	150	08/20/2001	A METHOD FOR SYNCHRONIZING GENERATION AND CONSUMPTION OF ISOCHRONOUS DATA	GULICK, DALE E.
<a href="#"><u>09998758</u></a>	6836813	150	11/30/2001	SWITCHING I/O NODE FOR CONNECTION IN A MULTIPROCESSOR COMPUTER SYSTEM	GULICK, DALE E.
<a href="#"><u>10005648</u></a>	Not Issued	30	12/03/2001	Embedded processor supporting both ACPI and ASF operations	GULICK, DALE E.
<a href="#"><u>10023233</u></a>	Not Issued	94	03/20/2002	MANAGING A CONTROLLER EMBEDDED IN A BRIDGE	GULICK, DALE E.
<a href="#"><u>10033142</u></a>	Not Issued	95	11/01/2001	MICROCOMPUTER BRIDGE ARCHITECTURE WITH AN EMBEDDED MICROCONTROLLER	GULICK, DALE E.
<a href="#"><u>10034560</u></a>	6791554	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE	GULICK, DALE E.
<a href="#"><u>10034878</u></a>	6697890	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED I/O INTERFACE	GULICK, DALE E.
<a href="#"><u>10034967</u></a>	6857033	150	12/27/2001	I/O NODE FOR A COMPUTER SYSTEM INCLUDING AN INTEGRATED GRAPHICS ENGINE AND AN INTEGRATED I/O HUB	GULICK, DALE E.
<a href="#"><u>10044707</u></a>	Not	41	01/11/2002	Processing tasks with failure recovery	GULICK, DALE E.

	Issued				
<a href="#"><u>10045117</u></a>	Not Issued	71	11/01/2001	Microcomputer bridge for remote manageability	GULICK, DALE E.
<a href="#"><u>10062390</u></a>	Not Issued	83	02/01/2002	Serial pin reflection of GPIO terminals	GULICK, DALE E.
<a href="#"><u>10066879</u></a>	Not Issued	71	02/04/2002	ASF state determination using chipset-resident watchdog timer	GULICK, DALE E.
<a href="#"><u>10066948</u></a>	Not Issued	41	02/04/2002	Remote management mechanism to prevent illegal system commands	GULICK, DALE E.
<a href="#"><u>10067175</u></a>	6892332	150	02/04/2002	HARDWARE INTERLOCK MECHANISM USING A WATCHDOG TIMER	GULICK, DALE E.
<a href="#"><u>10084596</u></a>	Not Issued	71	02/27/2002	Embedded processor with direct connection of security devices for enhanced security	GULICK, DALE E.
<a href="#"><u>10133297</u></a>	Not Issued	161	04/26/2002	Semiconductor device and method of controlling clock speed of a semiconductor device	GULICK, DALE E.
<a href="#"><u>10159346</u></a>	Not Issued	41	05/31/2002	Process load balancing	GULICK, DALE E.
<a href="#"><u>10180670</u></a>	6889262	150	06/26/2002	DIRECT TRANSACTION MODE FOR PERIPHERAL DEVICES	GULICK, DALE E.
<a href="#"><u>10184407</u></a>	6823403	150	06/27/2002	DMA MECHANISM FOR HIGH-SPEED PACKET BUS	GULICK, DALE E.
<a href="#"><u>10185171</u></a>	Not Issued	61	06/28/2002	Message based power management in a multi-processor system	GULICK, DALE E.
<a href="#"><u>10185195</u></a>	Not Issued	93	06/28/2002	MESSAGE BASED POWER MANAGEMENT	GULICK, DALE E.
<a href="#"><u>10198637</u></a>	Not Issued	41	07/18/2002	Use of a signal line to adjust width and/or frequency of a communication link during system operation	GULICK, DALE E.
<a href="#"><u>10209484</u></a>	Not Issued	93	07/31/2002	METHOD AND APPARATUS FOR IMPEDANCE MATCHING IN SYSTEMS CONFIGURED FOR MULTIPLE PROCESSORS	GULICK, DALE E.
<a href="#"><u>10283554</u></a>	Not Issued	61	10/30/2002	USB host controller	GULICK, DALE E.
<a href="#"><u>10283555</u></a>	Not Issued	93	10/30/2002	TRANSACTION DURATION MANAGEMENT IN A USB HOST CONTROLLER	GULICK, DALE E.
<a href="#"><u>10283612</u></a>	Not Issued	41	10/30/2002	Serial bus host controller diagnosis	GULICK, DALE E.
<a href="#"><u>10283733</u></a>	6944725	150	10/30/2002	RECIPROCALLY ADJUSTABLE DUAL QUEUE MECHANISM	GULICK, DALE E.
<a href="#"><u>10419082</u></a>	Not Issued	30	04/18/2003	Computer system including a secure execution mode-capable CPU and a security services processor connected via a secure communication path	GULICK, DALE E.
<a href="#"><u>10419121</u></a>	Not Issued	20	04/18/2003	Initialization of a computer system including a secure execution mode-	GULICK, DALE E.

				capable processor	
<u>10429132</u>	Not Issued	30	05/02/2003	Computer system including a bus bridge for connection to a security services processor	GULICK, DALE E.
<u>10464966</u>	Not Issued	30	06/19/2003	Controlling the replacement of prefetched descriptors in a cache	GULICK, DALE E.
<u>10628715</u>	Not Issued	20	07/28/2003	Flexible probe/probe response routing for maintaining coherency	GULICK, DALE E.
<u>10647397</u>	Not Issued	61	08/25/2003	Configuring a communication link interface	GULICK, DALE E.
<u>10654734</u>	Not Issued	41	09/04/2003	Computer system employing a trusted execution environment including a memory controller configured to clear memory	GULICK, DALE E.
<u>10842296</u>	Not Issued	30	05/10/2004	System including a host connected to a plurality of memory modules via a serial memory interconnect	GULICK, DALE E.
<u>60306361</u>	Not Issued	159	07/18/2001	Message based power management	GULICK, DALE E.
<u>60373571</u>	Not Issued	159	04/18/2002	Secure execution mode functional specification	GULICK, DALE E.
<u>60470078</u>	Not Issued	159	05/13/2003	Memory interface	GULICK, DALE E.
<u>07035683</u>	Not Issued	166	04/03/1987	DATA LINK CONTROLLER WITH FLEXIBLE MULTIPLEXER	GULICK, DALE E.
<u>07035684</u>	Not Issued	166	04/02/1987	ENHANCED UNIVERSAL ASYNCHRONOUS RECEIVER-TRANSMITTER	GULICK, DALE E.
<u>07035687</u>	4809269	150	04/02/1987	DUAL-PORT TIMING CONTROLLER	GULICK, DALE E.
<u>07035817</u>	4852088	150	04/03/1987	PACKET-AT-A-TIME REPORTING IN A DATA LINK CONTROLLER	GULICK, DALE E.
<u>07111475</u>	4860285	150	10/21/1987	MASTER/SLAVE SYNCHRONIZER	GULICK, DALE E.
<u>07311411</u>	5048012	150	02/14/1989	DATA LINK CONTROLER WITH FLEXIBLE MULTIPLEXER	GULICK, DALE E.
<u>07368083</u>	4907225	150	06/16/1989	DATA PROTOCOL CONTROLLER	GULICK, DALE E.
<u>07443088</u>	4949333	150	11/27/1989	ENHANCED UNIVERSAL ASYNCHRONOUS RECEIVER-TRANSMITTER	GULICK, DALE E.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
07586910	Not Issued	161	09/24/1990	PACKET-AT-A-TIME REPORTING IN A DATA LINK CONTROLLER	GULICK, DALE E.
07588194	5247621	150	09/26/1990	SYSTEM AND METHOD FOR PROCESSOR BUS USE	GULICK, DALE E.
07589065	5220601	150	09/27/1990	KEYPAD STATUS REPORTING SYSTEM	GULICK, DALE E.
07589090	5235635	150	09/27/1990	KEYPAD MONITOR WITH KEYPAD ACTIVITY-BASED ACTIVATION	GULICK, DALE E.
07589327	5199064	150	12/03/1990	FULLY-INTEGRATED TELEPHONE UNIT	GULICK, DALE E.
07589380	5298921	150	09/27/1990	MICROPROCESSOR TO EXTERNAL DEVICE SERIAL BUS COMMUNICATION SYSTEM	GULICK, DALE E.
07917489	5408639	150	07/21/1992	EXTERNAL MEMORY ACCESS CONTROL FOR A PROCESSING SYSTEM	GULICK, DALE E.
07917497	5448743	150	07/21/1992	GENERAL I/O PORT INTERRUPT MECHANISM	GULICK, DALE E.
07917503	Not Issued	166	07/21/1992	APPARATUS AND METHOD FOR DISABLING INTERRUPT MASKS IN PROCESSORS OR THE LIKE	GULICK, DALE E.
07918621	5420904	150	07/21/1992	SIGNAL AVERAGER	GULICK, DALE E.
07918622	Not Issued	166	07/21/1992	CLOCK GENERATOR CAPABLE OF SHUT-DOWN MODE AND CLOCK GENERATION METHOD	GULICK, DALE E.
07918624	5404459	150	07/21/1992	SERIAL INTERFACE MODULE AND METHOD IN WHICH THE CLOCK IS ONLY ACTIVATED TO SEND A PREDETERMINED NUMBER OF DATA BITS	GULICK, DALE E.
07918625	Not Issued	161	07/21/1992	KEYPAD SCANNER PROCESS AND DEVICE AND CORDLESS TELEPHONE EMPLOYING THE MECHANISM	GULICK, DALE E.
07918626	Not Issued	168	07/21/1992	MODULATOR TEST SYSTEM	GULICK, DALE E.
07918627	5555287	150	07/21/1992	INTEGRATED CIRCUIT AND	GULICK, DALE E.

				CORDLESS TELEPHONE USING THE INTEGRATED CIRCUIT	
<u>07918631</u>	Not Issued	166	07/21/1992	LOW POWER EMERGENCY TELEPHONE MODE	GULICK, DALE E.
<u>07918632</u>	Not Issued	166	07/21/1992	IN-CIRCUIT EMULATION CAPABILITY MODE IN INTEGRATED CIRCUIT AND CORDLESS TELEPHONE USING THE INTEGRATED CIRCUIT	GULICK, DALE E.
<u>07992571</u>	Not Issued	166	12/18/1992	HIGH-LEVEL DATA LINK CONTROLLER (HDLC) RECEIVER	GULICK, DALE E.
<u>08095923</u>	<u>5621402</u>	150	07/21/1993	KEYPAD SCANNER	GULICK, DALE E.
<u>08185697</u>	<u>5483577</u>	150	01/24/1994	SINGLE CHIP TELEPHONE ANSWERING MACHINE, TELEPHONE, SPEAKERPHONE, AND ADSI CONTROLLER	GULICK, DALE E.
<u>08201077</u>	<u>5502689</u>	150	02/24/1994	CLOCK GENERATOR CAPABLE OF SHUT-DOWN MODE AND CLOCK GENERATION METHOD	GULICK, DALE E.
<u>08316803</u>	<u>5845085</u>	150	10/03/1994	A SYSTEM FOR RECEIVING A DATA STREAM OF SERIALIZED DATA	GULICK, DALE E.
<u>08320778</u>	<u>5495530</u>	150	10/11/1994	LOW POWER EMERGENCY TELEPHONE MODE	GULICK, DALE E.
<u>08333387</u>	Not Issued	166	11/02/1994	AUDIO PROCESSING CHIP WITH EXTERNAL SERIAL PORT	GULICK, DALE E.
<u>08333451</u>	Not Issued	161	11/02/1994	MONOLITHIC PC AUDIO CIRCUIT	GULICK, DALE E.
<u>08333536</u>	<u>5659466</u>	150	11/02/1994	NONOLITHIC PC AUDIO CIRCUIT WITH ENHANCED DIGITAL WAVE TABLE AUDIO SYNTHESIZER	GULICK, DALE E.
<u>08346834</u>	<u>5530597</u>	150	11/30/1994	INTERRUPT MASK DISABLE CIRCUIT AND METHOD	GULICK, DALE E.
<u>08374191</u>	<u>5993057</u>	150	01/18/1995	APPARATUS FOR DETECTING AND AVERAGING DATA IN A DIGITAL DATA STREAM	GULICK, DALE E.
<u>08387389</u>	Not Issued	166	02/13/1995	INTEGRATED CIRCUIT, HAVING MICROPROCESSOR AND TELEPHONY PERIPHERAL CIRCUITRY HAVING CAPABILITY OF BEING PLACED IN PARTIAL EMULATION MODE	GULICK, DALE E.
<u>08462776</u>	Not Issued	162	06/05/1995	KEYPAD SCANNER	GULICK, DALE E.
<u>08463683</u>	<u>6122313</u>	150	06/05/1995	SELF-TESTING DATA MODULATOR	GULICK, DALE E.
<u>08464072</u>	<u>5512893</u>	150	06/05/1995	KEYPAD SCANNER PROCESS AND DEVICE AND CORDLESS	GULICK, DALE E.

TELEPHONE EMPLOYING THE MECHANISM					
08465345	Not Issued	161	06/05/1995	INTEGRATED CIRCUIT AND CORDLESS TELEPHONE USING THE INTEGRATED CIRCUIT	GULICK, DALE E.
08474554	5748983	150	06/07/1995	COMPUTER SYSTEM HAVING A DEDICATED MULTIMEDIA ENGINE AND MULTIMEDIA MEMORY HAVING ARBITRATION LOGIC WHICH GRANTS MAIN MEMORY ACCESS TO EITHER THE CPU OR MULTIMEDIA ENGINE	GULICK, DALE E.
08526482	5758177	150	09/11/1995	COMPUTER SYSTEM HAVING SEPARATE DIGITAL AND ANALOG SYSTEM CHIPS FOR IMPROVED PERFORMANCE	GULICK, DALE E.
08526488	5797028	150	09/11/1995	COMPUTER SYSTEM HAVING AN IMPROVED DIGITAL AND ANALOG CONFIGURATION	GULICK, DALE E.
08526531	5692211	150	09/11/1995	COMPUTER SYSTEM AND METHOD HAVING A DEDICATED MULTIMEDIA ENGINE AND INCLUDING SEPARATE COMMAND AND DATA PATHS	GULICK, DALE E.
08526595	5812800	150	09/11/1995	COMPUTER SYSTEM WHICH INCLUDES A LOCAL EXPANSION BUS AND A DEDICATED REAL-TIME BUS AND INCLUDING A MULTIMEDIA MEMORY FOR INCREASED MULTI-MEDIA PERFORMANCE	GULICK, DALE E.
08526596	5784592	150	09/11/1995	COMPUTER SYSTEM WHICH INCLUDES A LOCAL EXPANSION BUS AND A DEDICATED REAL-TIME BUS FOR INCREASED MULTIMEDIA PERFORMANCE	GULICK, DALE E.
08526769	5784650	150	09/11/1995	SYSTEM FOR INCREASING MULTIMEDIA PERFORMANCE AND OTHER REAL TIME APPLICATIONS BY INCLUDING A LOCAL EXPANSION BUS AND A MULTIMEDIA BUS ON THE COMPUTER SYSTEM MOTHERBOARD	GULICK, DALE E.
08561364	5778200	150	11/21/1995	BUS ARBITER INCLUDING AGING FACTOR COUNTERS TO DYNAMICALLY VARY ARBITRATION PRIORITY	GULICK, DALE E.
08621397	5763801	150	03/25/1996	COMPUTER SYSTEM AND METHOD FOR PERFORMING WAVE TABLE MUSIC SYNTHESIS WHICH STORES WAVE TABLE	GULICK, DALE E.

DATA IN SYSTEM MEMORY					
08622472	5717154	150	03/25/1996	COMPUTER SYSTEM AND METHOD FOR PERFORMING WAVE TABLE MUSIC SYNTHESIS WHICH STORES WAVE TABLE DATA IN SYSTEM MEMORY EMPLOYING A HIGH PRIORITY I/O BUS REQUEST MECHANISM FOR IMPROVED AUDIO FIDELITY	GULICK, DALE E.
08622761	5689080	150	03/25/1996	COMPUTER SYSTEM AND METHOD FOR PERFORMING WAVE TABLE MUSIC SYNTHESIS WHICH STORES WAVE TABLE DATA IN SYSTEM MEMORY WHICH MINIMIZES AUDIO INFIDELITY DUE TO WAVE TABLE DATA ACCESS LATENCY	GULICK, DALE E.
08623850	5809342	150	03/25/1996	COMPUTER SYSTEM AND METHOD FOR GENERATING DELAY-BASED AUDIO EFFECTS IN A WAVE TABLE MUSIC SYNTHESIZER WHICH STORES WAVE TABLE DATA IN SYSTEM MEMORY	GULICK, DALE E.
08650941	5898892	150	05/17/1996	COMPUTER SYSTEM WITH A DATA CACHE FOR PROVIDING REAL-TIME MULTIMEDIA DATA TO A MULTIMEDIA ENGINE	GULICK, DALE E.
08719801	Not Issued	161	09/25/1996	MULTIMEDIA DATA CONTROLLER	GULICK, DALE E.
08729638	5774683	150	10/21/1996	INTERCONNECT BUS CONFIGURED TO IMPLEMENT MULTIPLE TRANSFER PROTOCOLS	GULICK, DALE E.
08731825	5761457	150	10/21/1996	INTER-CHIP BUS WITH FAIR ACCESS FOR MULTIPLE DATA PIPES	GULICK, DALE E.
08731829	5771360	150	10/21/1996	PCI BUS TO TARGET INTEGRATED CIRCUIT INTERCONNECT MECHANISM ALLOWING MULTIPLE BUS MASTERS AND TWO DIFFERENT PROTOCOLS ON THE SAME BUS	GULICK, DALE E.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08731851	5832242	150	10/21/1996	INTER-CHIP BUS WITH EQUAL ACCESS BETWEEN MASTERS WITHOUT ARBITRATION	GULICK, DALE E.
08731852	5805845	150	10/21/1996	METHOD FOR LOADING MEMORY WITH PROGRAM AND DATA INFORMATION FROM PC MEMORY ACROSS A BRIDGING BUS	GULICK, DALE E.
08731956	5818948	150	10/23/1996	ARCHITECTURE FOR A UNIVERSAL SERIAL BUS-BASED PC SPEAKER CONTROLLER	GULICK, DALE E.
08733941	5854910	150	10/21/1996	METHOD FOR ACCESSING CONTROL AND STATUS REGISTERS ACROSS A PEER-PEER BUS	GULICK, DALE E.
08734295	5832245	150	10/21/1996	METHOD FOR ISOCHRONOUS FLOW CONTROL ACROSS AN INTER-CHIP BUS	GULICK, DALE E.
08734725	5764997	150	10/21/1996	SYSTEM FOR GENERATING INTERRUPT REQUESTS FROM EITHER SIDE OF AN INTER-CHIP BUS	GULICK, DALE E.
08734754	5898848	150	10/21/1996	INTER-CHIP BUS STRUCTURE FOR MOVING MULTIPLE ISOCHRONOUS DATA STREAMS BETWEEN INTEGRATED CIRCUITS	GULICK, DALE E.
08735683	6122749	150	10/23/1996	BUS BRIDGE WITH UNIFIED SERIAL BUS CONTROLLER HAVING POWER MANAGEMENT CAPABILITIES	GULICK, DALE E.
08740017	6216052	150	10/23/1996	NOISE ELIMINATION IN A USB CODEC	GULICK, DALE E.
08740019	5914877	150	10/23/1996	USB BASED MICROPHONE SYSTEM	GULICK, DALE E.
08753719	5809466	250	11/27/1996	AUDIO PROCESSING CHIP WITH EXTERNAL SERIAL PORT	GULICK, DALE E.
08770016	5778218	150	12/19/1996	METHOD AND APPARATUS FOR CLOCK SYNCHRONIZATION ACROSS AN ISOCHRONOUS BUS BY ADJUSTMENT OF FRAME CLOCK RATES	GULICK, DALE E.
08802321	6058443	150	02/18/1997	SYSTEM FOR PARTITIONING PC CHIPSET FUNCTIONS INTO LOGIC	GULICK, DALE E.

AND PORT INTEGRATED CIRCUITS					
08802323	5926629	150	02/18/1997	CONTINUOUSLY OPERATING INTERCONNECTION BUS	GULICK, DALE E.
08843796	5790833	150	04/21/1997	INTEGRATED CIRCUIT, HAVING MICROPROCESSOR AND TELEPHONY PERIPHERAL CIRCUITRY, HAVING CAPABILITY OF BEING PLACED IN PARTIAL EMULATION MODE	GULICK, DALE E.
08866644	6012111	150	05/30/1997	PC CHIPSET WITH INTEGRATED CLOCK SYNTHESIZER	GULICK, DALE E.
08866652	6101560	150	05/30/1997	PARTITIONED PC GAME PORT	GULICK, DALE E.
08906328	5944801	150	08/05/1997	ISOCHRONOUS BUFFERS FOR MMX-EQUIPPED MICROPROCESSORS	GULICK, DALE E.
08906329	5958027	150	08/05/1997	METHOD AND SYSTEM FOR OPTIMIZING THE FLOW OF ISOCHRONOUS DATA AND CLOCK RATE INFORMATION	GULICK, DALE E.
08928034	5987560	150	09/11/1997	INTEGRATED PROGRAMMABLE LOGIC CIRCUIT FOR CONDITIONING RECEIVED INPUT SIGNALS, DETECTING TRANSITIONS OF CONDITIONED SIGNALS, AND GENERATING AN ASSOCIATED INTERRUPT RESPECTIVELY	GULICK, DALE E.
08928035	6032213	150	09/11/1997	PC CORE LOGIC CHIPSET COMPRISING A SERIAL REGISTER ACCESS BUS	GULICK, DALE E.
08934969	6272465	150	09/22/1997	MONOLITHIC PC AUDIO CIRCUIT	GULICK, DALE E.
08955309	5941976	250	10/20/1997	INTERRUPT REQUEST DEASSERTION INTERLOCK MECHANISM	GULICK, DALE E.
08955326	5974492	150	10/20/1997	METHOD FOR INPUT/OUTPUT PORT REPLICATION USING AN INTERCONNECTION BUS	GULICK, DALE E.
08955327	6263385	150	10/20/1997	PC PARALLEL PORT STRUCTURE PARTITIONED BETWEEN TWO INTEGRATED CIRCUITS INTERCONNECTED BY A SERIAL BUS	GULICK, DALE E.
08976303	5999476	150	11/21/1997	BIOS MEMORY AND MULTIMEDIA DATA STORAGE COMBINATION	GULICK, DALE E.
09024293	6044414	150	02/17/1998	SYSTEM FOR PREVENTING A DMA CONTROLLER FROM EVALUATING ITS DRQ INPUT ONCE A DMA OPERATION HAS STARTED UNTIL THE DRQ INPUT HAS BEEN UPDATED	GULICK, DALE E.
09093865	6421702	150	06/09/1998	INTERRUPT DRIVEN ISOCHRONOUS	GULICK, DALE E.

TASK SCHEDULER SYSTEM					
09093866	6704763	150	06/09/1998	HARDWARE ENFORCEMENT MECHANISM FOR AN ISOCHRONOUS TASK SCHEDULER	GULICK, DALE E.
09094004	6418459	150	06/09/1998	ISOCHRONOUS TASK SCHEDULING STRUCTURE FOR A NON-REAL-TIME OPERATING SYSTEM	GULICK, DALE E.
09094168	6766347	150	06/09/1998	BI-LEVEL ISOCHRONOUS TASK SCHEDULING	GULICK, DALE E.
09098360	6151651	150	06/17/1998	A communication link with isochronous and asynchronous priority modes coupling bridge circuits in a computer system	GULICK, DALE E.
09098642	6088748	150	06/17/1998	PERSONAL COMPUTER SYSTEM INCORPORATING AN ISOCHRONOUS MULTI-CHANNEL, MULTI-RATE DATA BUS	GULICK, DALE E.
09098655	6085270	150	06/17/1998	MULTI-CHANNEL, MULTI-RATE ISOCHRONOUS DATA BUS	GULICK, DALE E.
09098656	6404771	150	06/17/1998	CLOCK LEAD/LAG EXTRACTION IN AN ISOCHRONOUS DATA BUS	GULICK, DALE E.
09098854	6199132	150	06/17/1998	COMMUNICATION LINK WITH ISOCHRONOUS AND ASYNCRONOUS PRIORITY MODES	GULICK, DALE E.
09099228	6148357	150	06/17/1998	AN INTEGRATED CPU AND MEMORY CONTROLLER UTILIZING A COMMUNICATION LINK HAVING ISOCHRONOUS AND ASYNCHRONOUS PRIORITY MODES	GULICK, DALE E.
09109440	6279058	150	07/02/1998	MASTER ISOCHRONOUS CLOCK STRUCTURE HAVING A CLOCK CONTROLLER COUPLING TO A CPU AND TWO DATA BUSES	GULICK, DALE E.
09109822	6202164	150	07/02/1998	DATA RATE SYNCHRONIZATION BY FRAME RATE ADJUSTMENT	GULICK, DALE E.
09109835	6061802	150	07/02/1998	SOFTWARE BASED CLOCK SYNCHRONIZATION	GULICK, DALE E.
09137345	6336179	150	08/21/1998	DYNAMIC SCHEDULING MECHANISM FOR AN ASYNCHRONOUS/ISOCHRONOUS INTEGRATED CIRCUIT INTERCONNECT BUS	GULICK, DALE E.
09217367	6631401	150	12/21/1998	FLEXIBLE PROBE/PROBE RESPONSE ROUTING FOR MAINTAINING COHERENCY	GULICK, DALE E.
09326304	6618782	150	06/04/1999	COMPUTER INTERCONNECTION BUS LINK LAYER	GULICK, DALE E.
09330527	6470410	150	06/11/1999	TARGET SIDE CONCENTRATOR MECHANISM FOR CONNECTING MULTIPLE LOGICAL PIPES TO A SINGLE FUNCTION UTILIZING A	GULICK, DALE E.

COMPUTER INTERCONNECTION BUS					
<u>09330528</u>	6457084	150	06/11/1999	TARGET SIDE DISTRIBUTOR MECHANISM FOR CONNECTING MULTIPLE FUNCTIONS TO A SINGLE LOGICAL PIPE OF A COMPUTER INTERCONNECTION BUS	GULICK, DALE E.
<u>09330529</u>	Not Issued	161	06/11/1999	SOURCE SIDE CONCENTRATOR MECHANISM FOR CONCENTRATING DATA AND CONTROL STREAMS FROM MULTIPLE FUNCTIONS FOR COMMUNICATION ACROSS A SINGLE LOGICAL PIPE OF A COMPUTER COMMUNICATION LINK	GULICK, DALE E.
<u>09330635</u>	6690676	150	06/11/1999	NON-ADDRESSED PACKET STRUCTURE CONNECTING DEDICATED END POINTS ON A MULTI-PIPE COMPUTER INTERCONNECT BUS	GULICK, DALE E.
<u>09330636</u>	6457081	150	06/11/1999	PACKET PROTOCOL FOR READING AN INDETERMINATE NUMBER OF DATA BYTES ACROSS A COMPUTER INTERCONNECTION BUS	GULICK, DALE E.
<u>09330637</u>	6421751	150	06/11/1999	DETECTING A NO-TAGS-FREE CONDITION IN A COMPUTER SYSTEM HAVING MULTIPLE OUTSTANDING TRANSACTIONS	GULICK, DALE E.
<u>09349563</u>	6499079	150	07/08/1999	SUBORDINATE BRIDGE STRUCTURE FOR A POINT-TO-POINT COMPUTER INTERCONNECTION BUS	GULICK, DALE E.

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<a href="#">09396025</a>	<a href="#">6611891</a>	150	09/15/1999	COMPUTER RESOURCE CONFIGURATION MECHANISM ACROSS A MULTI-PIPE COMMUNICATION LINK	GULICK, DALE E.
<a href="#">09431364</a>	<a href="#">6557048</a>	150	11/01/1999	COMPUTER SYSTEM IMPLEMENTING A SYSTEM AND METHOD FOR ORDERING INPUT/OUTPUT (IO) MEMORY OPERATIONS WITHIN A COHERENT PORTION THEREOF	GULICK, DALE E.
<a href="#">09501889</a>	<a href="#">6651128</a>	150	02/10/2000	SYSTEMS AND METHODS FOR ARBITRATING BETWEEN ASYNCHRONOUS AND ISOCHRONOUS DATA FOR ACCESS TO DATA TRANSPORT RESOURCES	GULICK, DALE E.
<a href="#">60109589</a>	Not Issued	159	11/23/1998	COMPUTER COMMUNICATION LINK	GULICK, DALE E.
<a href="#">11161792</a>	Not Issued	20	08/17/2005	MAGNETIC CONTROL DEVICE	GULICK, DALE M.
<a href="#">11161797</a>	Not Issued	30	08/17/2005	METHOD OF JOINING A SINTERED MAGNET TO A PIVOT ARM	GULICK, DALE M.

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